

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1.-21. (Canceled)

22. (Previously Presented) A receiver for estimation and compensation of phase imbalance or gain imbalance, the receiver comprising:

a QPSK modulation circuit based on a complex scrambling code;

a first circuit adapted to estimate the phase imbalance or gain imbalance of I and Q components of an incoming complex signal prior to symbol synchronization, and first circuit adapted to generate as an output a ratio of the product of compensated I and Q components and the square of the compensated I component; and

a second circuit that receives as inputs the uncompensated I and Q components and the output of the first circuit and outputs the compensated I and Q components.

23. (Previously Presented) The receiver of claim 22, wherein the first circuit receives as input the I and Q components of the complex signal after demodulation and compensation.

24. (Previously Presented) The receiver according to claim 22, wherein the first circuit comprises a low pass filter for low pass filtering of the product of the compensated I and Q components and low pass filtering of the square of the compensated I component.

25. (Previously Presented) The receiver of claim 22, comprising a synchronizer having inputs coupled to the outputs of the second circuit, the synchronizer comprising a UMTS synchronizer.

26. (Previously Presented) The receiver of claim 22, wherein the first circuit comprises a first multiplier receiving the compensated I and Q signals and generating a product thereof, and a second multiplier that generates the square of the compensated I component, a divider that divides the product of the first multiplier by the product of the second multiplier, the output of the divider integrated at an integration circuit that outputs an integration signal to the second circuit.

27.-32 (Canceled)